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TRANSMITTAL FORM (to be used for all correspondence after initial filing)		Application Number		09/255,777		
		Filing Date	Feb	February 23, 1999		
		First Named Inventor	Shu	Shunpei YAMAZAKI et al.		
		Group Art Unit	281	2812		
		Examiner Name	Ricl	Richard A. Booth		
Total Number of Pages in This Submission		Attorney Docket Number	740	756-1936		
ENCLOSURES (check all that apply)						
Fee Transmittal Form Fee Attached Amendment / Reply After Final Affidavits/declaration(s) Extension of Time Request Express Abandonment Request Information Disclosure Statement Certified Copy of Priority Document(s) Response to Missing Parts/ Incomplete Application Response to Missing Parts under 37 CFR 1.52 or 1.53	Assignm (for an A) Drawing Declarat Licensin Petition Applicat Power o Change Termina Request	ment Papers Application) g(s) ion and Power of Attorney ng-related Papers to Convert to a Provisional	Other	rized to charge any ad	APR 18 2002 APR 18 2002 Additional fees	
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT						
Firm Jeffrey L. Costellia, Reg. No. 35,483 Nixon Peabody LLP 8180 Greensboro Drive Suite 800 McLean VA 22102 Signature						
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PATENT A FOCE! 740756-1936 4 / 1867

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Shunpei YAMAZAKI et al.

Serial No. 09/255,777

Filed: February 23, 1999

For: SEMICONDUCTOR DEVICE AND

METHOD FOR FORMING THE SAME

Group Art Unit: 2812

) Examiner: Richard A. Booth

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents,

Washington, D.C. 20231, on <u>April 9, 2002</u>.

RESPONSE TO NON-FINAL OFFICE ACTION

Honorable Commissioner of Patents Washington, D.C. 20231

Dear Sir:

In response to the Examiner's non-final Office Action mailed January 9, 2002 please consider the following remarks in connection with the above-identified application.

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the present application.

The Examiner's non-final Office Action dated January 9, 2002 has been received and its contents carefully noted. Claims 12, 13, 15-19, 21-24, 26-43, 46-58, 60, 61, 65-71 and 75-84 are pending in the present application.

The Claims are Patentable Over the Prior Art;

The Examiner has not made a Prima Facie Case of Obviousness

Claims 12, 13, 15, 17, 23, 24, 26, 28-31, 33-39, 41-43, 46, 48-58, 60, 61, 65, 67-71, 75 and 77-84 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Chang, U.S. Patent No. 5,064,775 (Chang), Wolf et al., "Silicon Processing for VLSI Era, Vol. 1: Process Technology," (Wolf), Zdebel, U.S. Patent No. 5,154,946 (Zdebel), and Yamazaki et al., U.S.